

ECR #: P12

Tracker #: 16

Status: Ratified

Title: Change Reserved Address for Serial EEPROM

Release Date: September 17, 1997

Impact, Low: Riser designs

Spec Version: NLX Motherboard Specification 1.2

Summary:

The reserved serial EEPROM address for a riser configuration EEPROM needs to change from 1010000 to 1010111 to avoid conflicts with DIMMs on the motherboard. This allows the motherboard designer to utilize the system's SMBUS to read the EEPROM on risers.

Background:

An address for a serial EEPROM on the riser was reserved so that a standard EEPROM address would exist across NLX designs.

Change Current Specification As Shown:

In Table 4.11, "IDE, Floppy, and Front Panel Signal Descriptions," correct the Serial bus SCL signal so that the reserved EEPROM address is shown as 1010111. Only the underlined portion below is changed.

Table 4.11: IDE, Floppy, and Front Panel Signal Descriptions, continued

Signal	Pin	I/O	Description	Signal Type
Serial bus				
SDA	A150	I/O	serial data signal.	5V TTL
SCL	A151	O	serial clock signal. Note: That the NLX specification reserves EEPROM binary device address <u>1010111</u> for future riser configuration initiatives.	5V TTL